
ST25DV-I2C series with Open Drain GPO design improvement

What is the change?

The ST25DV04K, ST25DV16K and ST25DV64K with Open Drain General Purpose Output Dynamic NFC Tags have undergone a design change at metal mask layer about:

- The quality of internal load modulation generation to improve the tag's answer at all RF field conditions (from Hmin to Hmax).
- The RF-I2C arbitration mechanism during extreme I2C bus activity.
- The stability of on-going write operations during premature RF field cuts, when Vcc is on.

This product change is improving the overall system robustness and has no impact on the application design. No hardware or software change is required at application level.

Why?

The strategy of STMicroelectronics Memory division is to support our customers on a long-term basis and to apply continuous improvement.

In line with this commitment, those design enhancements will lead to:

- Improve interoperability with readers
- Increase system robustness
- Facilitate access from RF interface when the device is under huge activity in I2C bus

When?

Qualification samples will be available from week 07 / 2020 or week 11 / 2020, depending on product version.

Current version will be delivered until Week 26 / 2020.

From Week 27 / 2020, new version only will be delivered.

How will the change be qualified?

The change has been qualified following the standard ST Microelectronics Corporate Procedures for Quality and Reliability.

The **Qualification Report QRMMY1922** will be available Week 04 / 2020.

What is the impact of the change?

- **Form:** Marking change on package top side
- **Fit:** No change
- **Function:** No change

How can the change be seen?

- **BOX LABEL MARKING**

On the BOX LABEL MARKING, the difference is visible inside the **Finished Good Part Number**:
The mask revision digit is “**B**” for the design improvement, this identifier being “**A**” for the current version.

→ **Example for ST25DV64K-IER6S3**

The diagram shows a rectangular box representing a label. On the left side, the text "STMicroelectronics" is written vertically. Inside the box, the following information is displayed:

- TYPE: ST25DV64K-IER6S3**
- 25DV64IKER6S3U H B**
- Good Dice: XXXXXX
- Trace Codes *PPYWWLLL WX TF*
- Source lot X0000000
- Bulk ID X0X00XXX0000

Below the text is a barcode. At the bottom of the box, it says "Please provide the bulk ID for any inquiry".

Two callout boxes are present:

- A box pointing to the "B" in the part number: ". Mask revision:
- **B for design improvement**
- A for current version"
- A box pointing to the "U H" in the part number: "Assembly and Test & Finishing"

A box labeled "Process Technology" is positioned between the "Source lot" and "Bulk ID" fields.

How can the change be seen?

- DEVICE MARKING

The change is visible inside the **Product name** where the last digit is “**B**” for the design change, this identifier being “**A**” for the current version.

SO8N

Example for ST25DV64K:

New design

Current design

Legend:

DV6DERB = Product name

P = Assembly plant

Y = Assembly year

WW = Assembly week

U = Process techno (CMOS F8H)



TSSOP8

Example for ST25DV04K:

New design

Current design

Legend:

V2EUB = Product name

P = Assembly plant

Y = Assembly year

WW = Assembly week



DFN8

Example for ST25DV04K:

New design

Current design

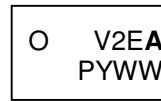
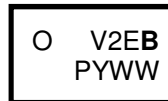
Legend:

V2EB = Product name

P = Assembly plant

Y = Assembly year

WW = Assembly week



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Appendix A: Product Change Information

Product family / Commercial products:	ST25DV-I2C series with Open-Drain GPO (See Appendix B)
Customer(s):	All
Type of change:	Design change
Reason for the change:	System robustness improvement
Description of the change:	Digital routing modification at metal mask layer
Forecast date of the change: (Notification to customer)	Week 01 / 2020
Forecast date of <u>Qualification samples</u> availability for customer(s):	Week 07 / 2020 or Week 11 / 2020 See Appendix B for details
Forecast date for the internal STMicroelectronics change, <u>Qualification Report</u> availability:	Qualification Report QRMMY1922 will be available Week 04 / 2020.
Marking to identify the changed product:	Die version digit “ B ” in top marking product name
Description of the qualification program:	Standard ST Microelectronics Corporate Procedures for Quality and Reliability
Product Line(s) and/or Part Number(s):	See information in Appendix B
Manufacturing location:	Rousset (France)
Estimated date of first shipment:	Week 27 / 2020

Appendix B: Concerned Commercial Part Numbers:

Current Commercial Part Numbers	Current Finished Good Part Numbers	New Finished Good Part Numbers	Qualification samples availability
ST25DV04K-IER6S3	25DV04KIER6S3UHA	25DV04KIER6S3UHB	Week 11 / 2020
ST25DV04K-IER6T3	25DV04KIER6T3UTA	25DV04KIER6T3UTB	Week 11 / 2020
ST25DV04K-IER6C3	25DV04KIER6C3UGA	25DV04KIER6C3UGB	Week 11 / 2020
ST25DV04K-IER8S3	25DV04KIER8S3UHA	25DV04KIER8S3UHB	Week 07 / 2020
ST25DV04K-IER8T3	25DV04KIER8T3UTA	25DV04KIER8T3UTB	Week 07 / 2020
ST25DV04K-IER8C3	25DV04KIER8C3UGA	25DV04KIER8C3UGB	Week 11 / 2020
ST25DV16K-IER6S3	25DV16KIER6S3UHA	25DV16KIER6S3UHB	Week 11 / 2020
ST25DV16K-IER6T3	25DV16KIER6T3UTA	25DV16KIER6T3UTB	Week 11 / 2020
ST25DV16K-IER8S3	25DV16KIER8S3UHA	25DV16KIER8S3UHB	Week 07 / 2020
ST25DV16K-IER8T3	25DV16KIER8T3UTA	25DV16KIER8T3UTB	Week 07 / 2020
ST25DV64K-IER6S3	25DV64KIER6S3UHA	25DV64KIER6S3UHB	Week 07 / 2020
ST25DV64K-IER6T3	25DV64KIER6T3UTA	25DV64KIER6T3UTB	Week 07 / 2020
ST25DV64K-IER8S3	25DV64KIER8S3UHA	25DV64KIER8S3UHB	Week 11 / 2020
ST25DV64K-IER8T3	25DV64KIER8T3UTA	25DV64KIER8T3UTB	Week 11 / 2020

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Document Revision History		
Date	Rev.	Description of the Revision
Dec. 16, 2019	1.00	First draft creation – Christian POLI

Source Documents & Reference Documents		
Source document Title	Rev.:	Date: