## PRODUCT / PROCESS CHANGE NOTIFICATION

| 1. PCN basic data                                 |  |  |
|---|--|--|
| 1.1 Company  STMicroelectronics International N.V |  | STMicroelectronics International N.V   |
| 1.2 PCN No.                                       |  | MMS/16/9604  |
| 1.3 Title of PCN                                  |  | STM32L04x 32K STM32L05 64K STM32L073x 192K products - new die minor revision |
| 1.4 Product Category                              |  | STM32L04x 32K & STM32L05 64K & STM32L073x 192K products                      |
| 1.5 Issue date                                    |  | 2016-02-23   |

| 2. PCN Team               |                     |  |
|---------------------------|---------------------|--|
| 2.1 Contact supplier      |                     |  |
| 2.1.1 Name                | SETTLES JEFF        |  |
| 2.1.2 Phone               | +44 1628896222      |  |
| 2.1.3 Email               | jeff.settles@st.com |  |
| 2.2 Change responsibility |                     |  |
| 2.2.1 Product Manager     | Michel BUFFA        |  |
| 2.1.2 Marketing Manager   | Daniel COLONNA      |  |
| 2.1.3 Quality Manager     | Pascal NARCHE       |  |

| 3. Change    |   |                            |
|--------------|---|----------------------------|
| 3.1 Category | 3.2 Type of change                          | 3.3 Manufacturing Location |
| Die redesign | Mask or mask set change with new die design | NA                         |

| 4. Description of change |   |   |  |
|--------------------------|---|---|--|
|                          | Old   | New   |  |
| 4.1 Description          | Limitations described in errata sheets.  - STM32L04x 32K: revision A or revision B Errata sheet Revision 1 for STM32L031x4/6 products Errata sheet Revision 1 for STM32L041x4/6 products  - STM32L05 64K: revision Y or revision Z Errata sheet Revision 4 for STM32L051x6/8 products Errata sheet Revision 4 for STM32L052x6/8 products Errata sheet Revision 4 for STM32L062K8 products Errata sheet Revision 4 for STM32L063x6/8 products Errata sheet Revision 3 for STM32L063x8 products  - STM32L073x 192K: revision A or revision B Errata sheet Revision 2 for STM32L071x8/B/Z products Errata sheet Revision 3 for STM32L072x8/B/Z products Errata sheet Revision 3 for STM32L072x8/B/Z products Errata sheet Revision 4 for STM32L073x8/B/Z products Errata sheet Revision 4 for STM32L073x8/B/Z products Errata sheet Revision 4 for STM32L073x8/B/Z products Errata sheet Revision 4 for STM32L083x8/B/Z products | ST MCD Division is pleased to announce that the features of STM32L04x 32K & STM32L05 64K & STM32L073x 192K products, will be enhanced through the introduction of new dice revisions.  I2C and USART wake up the device from Stop mode.  BFB2 bit selects boot from Bank 2 on die STM32L073x 192K.  - Die STM32L04x 32K: Revision Y, changes are described in Errata sheet Revision 2 for STM32L031x4/6 products  Errata sheet Revision 2 for STM32L041x4/6 products  - Die STM32L05 64K: Revision X, changes are described in Errata sheet Revision 5 for STM32L051x6/8 products  Errata sheet Revision 5 for STM32L052x6/8 products  Errata sheet Revision 5 for STM32L052x6/8 products  Errata sheet Revision 5 for STM32L063X8 products  Errata sheet Revision 4 for STM32L063X8 products  Errata sheet Revision 3 for STM32L071x8/B/Z products  Errata sheet Revision 3 for STM32L071x8/B/Z products  Errata sheet Revision 4 for STM32L072x8/B/Z products  Errata sheet Revision 4 for STM32L072x8/B/Z products  Errata sheet Revision 5 for STM32L073x8/B/Z products |  |

| 4.2 Anticipated Impact on form,fit, function, quality, reliability or | Function : improvements are indicated in Errata sheets |
|---|--|
| processability?   |  |

| 5. Reason / motivation for change |   |  |
|-----------------------------------|---|--|
|                                   | The strategy of ST MCD Division is to increase the robustness and improve performances, quality and functionality of our products. This is achieved by introducing new dice revisions for STM32L04x 32K, STM32L05 64K and STM32L073x 192K products. |  |
| 5.2 Customer Benefit              | QUALITY IMPROVEMENT   |  |

| 6. Marking of parts / traceability of change |  |  |
|--|--|--|
|  | The dice revisions change.  - It is indicated in register DBG_ID code at address: 0x4001 5800.  - It is marked on packages of the part, except on STM32L0xxxDIE0 products. |  |

| 7. Timing / schedule                |              |  |
|-------------------------------------|--------------|--|
| 7.1 Date of qualification results   | 2016-04-24   |  |
| 7.2 Intended start of delivery      | 2016-05-24   |  |
| 7.3 Qualification sample available? | Upon Request |  |

| 8. Qualification / Validation  |                            |               |            |
|--|----------------------------|---------------|------------|
| 8.1 Description 9604_417X_425Z_447Z Reliability plan - 447Z Reliability Report.pdf |                            |               |            |
| 8.2 Qualification report and qualification results                                 | Available (see attachment) | Issue<br>Date | 2016-02-23 |

| 9. Attachments (additional  | l documentations) |
|---|-------------------|
| 9604PpPrdtLst.pdf<br>9604_Additional information.pdf  |                   |
| 9604_Additional information.pdf<br>  9604_417X_425Z_447Z_Reliability_plan - 447Z_Reliability_Report.pdf |                   |

| 10. Affected parts      |                         |                          |
|-------------------------|-------------------------|--------------------------|
| 10. 1 Current           |                         | 10.2 New (if applicable) |
| 10.1.1 Customer Part No | 10.1.2 Supplier Part No | 10.1.2 Supplier Part No  |
|                         | STM32L052C6T6           |                          |
|                         | STM32L052K6U6           |                          |

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