

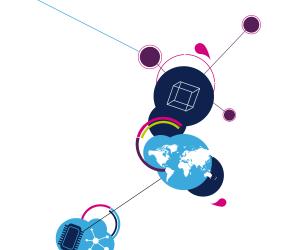
# PCN 9942: Qualification Plan Die 451XXXZ (Cut1.1)

### Reliability Evaluation Plan

Oct 6th 2016

MDG MCD Quality & Reliability Department





### PCN 9942: Qualification Plan: Die 451XXXZ (Cut 1.1)

Qualification plan for changes described in the PCN 9942

Reliability report: RERMCD1501

Trial	Test1	Method	Conditions	Sample x lot
				451XXXZ (Cut 1.1) / TFBGA216
DIE	LU	0018695 JESD78	130°C	6 x 1
	ESD HBM	0060102 ANSI/ESDA/ JEDEC JS-001	Ta 25°C 2KV (Class 2)	3 x 1
	ESD CDM	ANSI/ESD STM5.3.1	25°C 250V (Class C3)	3 x 1
	HTOL	MIL-STD-883 Method 1005 JESD22-A108	125°C , 3.6V 168h	77 x 1

 Note: This plan will be executed with TFBGA216 (DSI option). CDM will be done with the TFBGA216 DSI option and with the TFBGA216 no DSI option and will validate that there is no regression compared to previous cut and previous substrate.



## PRODUCT/PROCESS CHANGE NOTIFICATION PCN 9942 - Additional information

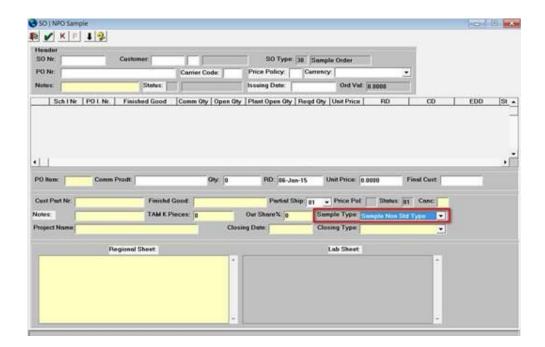
### STM32F7x 2MB - die minor revision & Optimized substrate layout on TFBGA package only

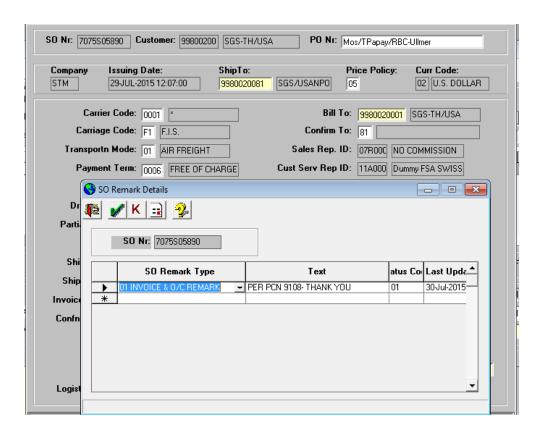
### **MMS - Microcontrollers Division (MCD)**

#### How to order samples?

For all sample request linked to this PCN, please:

- request sample(s) through Notice tool, indicating a single Commercial Product for each request.
- insert "PCN 9942" into the remarks of your order.
- place **non standard** sample order using the following field in your system.







#### **Public Products List**

PCN Title: STM32F7x 2MB - die minor revision & Optimized substrate layout on TFBGA package only

PCN Reference: MDG/16/9942
PCN Created on: 22-Sep-2016

Subject: Public Products List

Dear Customer,

Please find below the Standard Public Products List impacted by the change.

STM32F779NIH6	STM32F769IIT6	STM32F767VIT6
STM32F769NIH6	STM32F767NIH6	STM32F769AIY6TR
STM32F767IIT6	STM32F767IIK6	STM32F767ZIT6
STM32F767BIT6	STM32F769BIT6	STM32F777NIH6

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