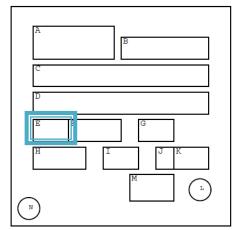


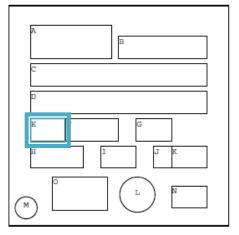
Transfer assembly plant from Stats ChipPAC Shanghai China (SCC) to Stats ChipPAC Jiangyin China (JSCC) for products in BGA package

MDG - Microcontrollers Division (MCD)

How can the change be seen?

For LFBGA 10x10x1.7 100 package, the marking instruction of the assembly plant indicated on the products is changing from GH (in E) to GQ (in E).

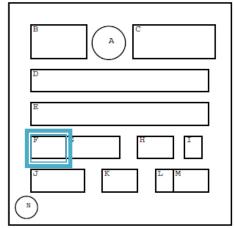




Example of previous marking

Example of new marking

For LFBGA 10x10x1.7 144 package, the marking instruction of the assembly plant indicated on the products is changing from GH (in F) to GQ (in F).



٥
X
3 K L M N
$^{\circ}$

Example of previous marking

Example of new marking

How to order samples?

For all sample request linked to this PCN, please:

- request sample(s) through Notice tool, indicating a single Commercial Product for each request.
- insert "PCN 9975" into the remarks of your order.
- place **non standard** sample order using the following field in your system.

B 🔨 K F 13							_				
So Nr:	Customer:		SO Type	30 Sample	Order	1					
PO Nr.		Carrier Code:	Price Policy.	Currency							
Notes:	Status	1	Issuing Date:		Ord Val	0.0000					
Sch1Nr POLNr	Finished Good	Comm Oty Open C	by Plant Open Oty	Regd Oly L	Init Price	RD		CD	Ì	EDD	51
	n Danette	Check line	00 44 1		Direr I	0.010	Einel	Out T	_	-	
PO Item: Com	n Prodt: Finishd (Geod.	RD: 06-Je Partial S		It Price:		Final (Cust	-		
PO Itom: Com		Good:		hip: 01 •	Price Pot		= 01 Cr	nnc:	1		
PO Nem: Com	Finishd	Good: a	Partial S	hip: 01 🔹	Price Pot	Statu	= 01 Cr	nnc:			
PO hom: Com Cust Part Nr. Votes: Project Name	Finishd	Good: a	Partial S Our Share %	hip: 01 • Samp Close	Price Pol	Statu	= 01 Cr	anc -			
PO hom: Com Cust Part Nr. Notes: Project Name	Finishd (TAM K P)	Good: a	Partial S Our Sham%: g osing Date:	hip: 01 • Samp Close	Price Pol le Type g Type	Statu	= 01 Cr	anc -			
PO hom: Com Cust Part Nr. Notes: Project Name	Finishd (TAM K P)	Good: leces: 0 CA	Partial S Our Sham%: g osing Date:	hip: 01 • Samp Close	Price Pol le Type g Type	Statu	= 01 Cr	anc:			
PO hom: Com Cust Part Nr. Notes: Project Name	Finishd (TAM K P)	Good: leces: 0 CA	Partial S Our Sham%: g osing Date:	hip: 01 • Samp Close	Price Pol le Type g Type	Statu	= 01 Cr	anc:			

SO Nr: 7075S058	990 Customer: 99800200 SGS-	TH/USA PO Nr: Mos/	Papay/RBC-Ullmer	
	ssuing Date: ShipT 19JUL-2015 12:07:00 99800		e Policy: Curr Code: 02 U.S. DOLLA	R
Carriage	Code: 0001 * Code: F1 F.I.S.	Confirm To:	980020001 SGS-TH/USA	
Transportn I Payment	Mode: 01 AIR FREIGHT Term: 0006 FREE OF CHARGE	Sales Rep. ID: [Cust Serv Rep ID: 1	7R00C NO COMMISSION 1A000 Dummy FSA SWISS	
Dr 🙀 I	Remark Details			
Shi Ship	SO Remark Type	Text PER PCN 9108- THANK YOU	atus Co Last Upda	
Invoice *				
Confn				1
				J

PCN 9975 - MCDRER1624 Qualification Plan LFBGA10x10 Transfer assembly plant from Stats ChipPAC Shanghai China (SCC) to Stats ChipPAC Jiangyin China (JSCC)

Dec 13th 2016

MDG MCD Quality & Reliability Department



PCN 9975- MCDRER1624 Qualification Plan JSCC plant for LFBGA10x10 package ²

- Qualification plan for changes described in the PCN 9975
- Reliability report: RERMCD1624
- Test Vehicles

Package	Package	Device	Diffusion	Number
line		(Partial RawLine	Process	of qual Lots
		Code)		
	144b	STR7 (X3*401)	M8T	1
LFBGA	144b	STM32 (X3*430)	TSMC 0.18µm	1
10x10	100b	STM32 (H0*414)	TSMC 0.18µm	1

Package Reliability Trials : (*) tests performed after preconditioning PCN 9975- RERMCD1624 JSCCLFBGA10x10 RELIABILITY TRIALS 3

Reliability Trial		Test Conditions	Pass Criteria	Unit per Lot	Lot qty
PC	Pre Conditioning: Moisture Sensitivity Jedec Level 3 J-STD-020/ JESD22-A113	Bake (125°C / 24 hrs) Soak (30°C / 60% RH / 192 hrs) for level 3 Convection reflow: 3 passes	3 passes MSL3	308	1/ device qual
AC or Uhast(*)	Autoclave JESD22 A102 or UnBiased Highly Accelerated Temperature and Humidity Stress JESD22 A118	121°C, 100% RH, 2 Atm 130°C, 85%RH, 2 atm	96h	77	1/ device qual
тС(*)	Thermal Cycling JESD22 A104	-50°C, +150°C Or equivalent -65°C +150°C	1000Cy 500Cy (1000cy/2000cy as monitoring)	77	1/ device qual
WPT/WBS After TC	Wire Bond Pull- Mil Std883 method 2011 Wire Bond Shear ,AECQ100-001	3g min pull strengh 15g min bond shear	500Cy 1000Cy 2000Cy		
THB(*)	Temperature Humidity Bias JESD22 A101	85°C, 85% RH, bias	1000h	77	1/ device qual
or HAST(*)	Biased Highly Accelerated temperature & humidity stress JESD22 A110	110°C, 1.2 atm , 85% RH bias	264h	77	
HTSL	High Temperature Storage Life	150°C- no bias	1000h	77	1/ device qual
Construction analysis including Solderability, Physical demensions	JESD 22B102 JESDB100/B108			15 10	1/ Lead frame and Front end technology
ESD	ESD Charge Device Model ANSI/ESD STM5.3.1	250V / 500V/ 750V depending on device datasheet	250V or 500V or 750V	3	1/ device qual

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of STMicroelectronics.

The ST logo is registered trademark of STMicroelectronics All other names are the property of their respective owners

© 2016 STMicroelectronics - All Rights Reserved

STMicroelectronics group of companies Australia - Brazil - Canada - China - Finland - France - Germany - Hong Kong -

India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States.

www.st.com



Public Products List

Publict Products are off the shelf products. They are not dedicated to specific customers, they are available through ST Sales team, or Distributors, and visible on ST.com

PCN Title : Transfer assembly plant from Stats ChipPAC Shanghai China (SCC) to Stats ChipPAC Jiangyin China (JSCC) for products in BGA package *PCN Reference :* MDG/17/9975

PCN Reference: MDG/17/9975

Subject : Public Products List

Dear Customer,

Please find below the Standard Public Products List impacted by the change.

STM32F103ZGH6JTR	STM32F103VCH6	STM32F103VBH6
STM32F103VEH6	STM32F103ZGH6TR	STM32F107VCH6
STM32F103VEH6TR	STM32F103VEH7	STM32F103ZEH6TR
STM32F103VDH6TR	STM32F103ZEH6	STM32F103ZGH6J
STM32F103ZFH6	STM32F103ZCH6	STM32F103ZGH6
STM32F103ZEH7	STR710RZH6	STM32F103ZFH6TR
STR710FZ2H6	STM32F103VBH7	STM32F103V8H6
STM32F103ZDH6	STR710FZ1H6	STM32F103ZGH7
STM32F103VDH6	STM32F103ZDH6TR	STM32F105VBH6

IMPORTANT NOTICE - PLEASE READ CAREFULLY

STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, enhancements, modifications, and improvements to ST products and/or to this document at any time without notice. Purchasers should obtain the latest relevant information on ST products before placing orders. ST products are sold pursuant to ST's terms and conditions of sale in place at the time of order acknowledgement.

Purchasers are solely responsible for the choice, selection, and use of ST products and ST assumes no liability for application assistance or the design of Purchasers' products.

No license, express or implied, to any intellectual property right is granted by ST herein.

Resale of ST products with provisions different from the information set forth herein shall void any warranty granted by ST for such product.

ST and the ST logo are trademarks of ST. All other product or service names are the property of their respective owners.

Information in this document supersedes and replaces information previously supplied in any prior versions of this document.

© 2016 STMicroelectronics - All rights reserved