


**PRODUCT / PROCESS CHANGE INFORMATION**

**1. PCI basic data**

<b>1.1 Company</b>		STMicroelectronics International N.V
<b>1.2 PCI No.</b>	MDG/23/14333	
<b>1.3 Title of PCI</b>	STM32H56x/57x and STM32H503 change of the memory configuration bits	
<b>1.4 Product Category</b>	STM32H503x, STM32H562x, STM32H563x, STM32H573x	
<b>1.5 Issue date</b>	2023-12-06	

**2. PCI Team**

<b>2.1 Contact supplier</b>	
<b>2.1.1 Name</b>	NEMETH KRISZTINA
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<b>2.2 Change responsibility</b>	
<b>2.2.1 Product Manager</b>	Ricardo Antonio DE SA EARP
<b>2.1.2 Marketing Manager</b>	Veronique BARLATIER
<b>2.1.3 Quality Manager</b>	Pascal NARCHE

**3. Change**

<b>3.1 Category</b>	<b>3.2 Type of change</b>	<b>3.3 Manufacturing Location</b>
General Product & Design	Firmware modification : firmware is programed in the memory without effect of electrical performance at the customer (bug fix)	ST Crolles 300 (France)

**4. Description of change**

	Old	New
<b>4.1 Description</b>	memory configuration bits wrong settings	Change in the memory configuration bits settings: - Addressable memory size is configured in some engineering registers not available in user mode. - Settings of those configuration bits were updated at production site to clearly differentiate 1M from 2M devices to conform to the device specification. - We expect very low impact at user side as information about those registers are not communicated in user documentations. - However in case the software application extends the CRC on those registers it will trigger a change.
<b>4.2 Anticipated Impact on form,fit, function, quality, reliability or processability?</b>	No change on product May impact customer in case CRC is done (it might impact customer production flow)	

**5. Reason / motivation for change**

<b>5.1 Motivation</b>	Those bits are non user mode bits - but in case of CRC application software may trigger a change.
<b>5.2 Customer Benefit</b>	SERVICE IMPROVEMENT

**6. Marking of parts / traceability of change**

<b>6.1 Description</b>	Traceability of the change is ensured by ST internal tools.
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**7. Timing / schedule**

<b>7.1 Date of qualification results</b>	2024-01-03
<b>7.2 Intended start of delivery</b>	2024-01-10

<b>7.3 Qualification sample available?</b>	Not Applicable
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<b>8. Qualification / Validation</b>			
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<b>8.1 Description</b>			
<b>8.2 Qualification report and qualification results</b>	In progress	<b>Issue Date</b>	

<b>9. Attachments (additional documentations)</b>			
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14333 Public product.pdf			
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<b>10. Affected parts</b>		
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10. 1 Current		10.2 New (if applicable)
10.1.1 Customer Part No	10.1.2 Supplier Part No	10.1.2 Supplier Part No
	STM32H503RBT6	
	STM32H563IIK6	
	STM32H563VIT6	
	STM32H563ZIT6	
	STM32H573VIT6	
	STM32H573ZIT6	

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