



## Product/Process Change Notice - PCN 12\_0103 Rev. -

Analog Devices, Inc. Three Technology Way Norwood, Massachusetts 02062-9106

This notice is to inform you of a change that will be made to certain ADI products (see Material Report). Any issues with this PCN or requirements to qualify the change (additional data or samples) must be sent to ADI within 30 days of publication date. ADI contact information is listed below.

**PCN Title:** Minor Metal Mask Revision and Datasheet Change on the AD9552  
**Publication Date:** 02-Aug-2012  
**Effectivity Date:** 31-Oct-2012 *(the earliest date that a customer could expect to receive changed material)*

### Revision Description:

Initial Release

### Description Of Change

#### Change #1:

Length of the calibration state machine increased.

Calibration state machine has been changed from only resetting with a power-on reset to being able to reset with a power on reset or a hard reset, by implementing a logical OR of these two resets to the calibration reset.

#### Change #2:

We allowed bypassing the state of the pin programmed doubler via SPI control by adding a Mux.

#### Change #3:

Changed the bit name of Register 0x00 bit [5] from 'Soft reset' to 'Register map reset'.

Changed the bit description of Register 0x00 bit [5] from 'Software initiated reset (register values set to default). This is an autoclearing bit.' to 'Resets the register map to the default values. This is an autoclearing bit.'

### Reason For Change

#### Reason for change #1:

Increasing the length of the calibration state machine improves the calibration accuracy.

A hard reset will now clear the calibration state machine under all conditions; this gives the ability to put the calibration state machine into a known state without the need to power cycle the part.

#### Reason for change #2:

This allows SPI control of the frequency doubler regardless of the pin programmable state. Previously if the frequency doubler was turned on with pin control options, it could not be turned off by SPI control.

#### Reason for change #3:

The bit name and description of Register 0x00 bit [5] were changed to more accurately represent the function of the bit and remove the ambiguity of the current phrasing which could be interpreted to imply that the bit performed functions other than resetting the register map to the default values.

### Impact of the change (positive or negative) on fit, form, function & reliability

The changes described above will not have any impact on the quality or reliability of the device.

### Product Identification *(this section will describe how to identify the changed material)*

All material ordered with a date code of 1229 and beyond will be with the new revision

**Summary of Supporting Information**

No qualification was required for this change.

**Supporting Documents**      None

**For questions on this PCN, send email to the regional contacts below or contact your local ADI sales representative**

**Americas:**    PCN\_Americas@analog.com

**Europe:**    PCN\_Europe@analog.com

**Japan:**      PCN\_Japan@analog.com

**Rest of Asia:**    PCN\_ROA@analog.com

**Appendix A - Affected ADI Models**

**Added Parts On This Revision - Product Family / Model Number (3)**

AD9552 / AD9552/PCBZ

AD9552 / AD9552BCPZ

AD9552 / AD9552BCPZ-REEL7

**Appendix B - Revision History**

<b>Rev</b>	<b>Publish Date</b>	<b>Effectivity Date</b>	<b>Rev Description</b>
Rev. -	02-Aug-2012	31-Oct-2012	Initial Release

Analog Devices, Inc.

DocId:1944 Parent DocId:None Layout Rev:6