



$t_R$ : Rise time for both SDA and SCL signals

I looked at the SDA rise time and it was 194ns, well within the data sheet specification range. I do not believe we should modify this specification entry.

SCL being generated by the controller does not make sense to check and specify.

$t_{HD;DAT}$ : Data hold time for I2C-bus devices

The AD9545  $t_{HD;DAT}$  min is 100 ns, which does not meet the I2C specification of 0  $\mu$ s min for both standard and fast modes. Also, the I2C specification has 3.45  $\mu$ s max in standard mode and 0.9  $\mu$ s in fast mode. AD9545 does not have any max value specified.