



PRODUCT/PROCESS CHANGE NOTIFICATION

PCN MMS-MIC/08/3937
Notification Date 08/07/2008

CMOSM6XF/DF PROCESS TECHNOLOGY TRANSFER TO CATANE M5 (ITALY)

Table 1. Change Implementation Schedule

Forecasted implementation date for change	20-Oct-2008
Forecasted availability date of samples for customer	20-Oct-2008
Forecasted date for STMicroelectronics change Qualification Plan results availability	20-Oct-2008
Estimated date of changed product first shipment	06-Nov-2008

Table 2. Change Identification

Product Identification (Product Family/Commercial Product)	See page 5
Type of change	Waferfab location change
Reason for change	Phoenix fab closure
Description of the change	Add Catane M5 fab (Italy) as a qualified wafer fab. All datasheet parameters are identical to Phoenix silicon.
Product Line(s) and/or Part Number(s)	See attached
Description of the Qualification Plan	See attached
Change Product Identification	Package marking from "VP" to "V5"
Manufacturing Location(s)	

Table 3. List of Attachments

Customer Part numbers list	
Qualification Plan results	



Customer Acknowledgement of Receipt		PCN MMS-MIC/08/3937
Please sign and return to STMicroelectronics Sales Office		Notification Date 08/07/2008
☐ Qualification Plan Denied ☐ Qualification Plan Approved ☐ Change Denied ☐ Change Approved	Name:	
	Title:	
	Company:	
	Date:	
	Signature:	
Remark		

DOCUMENT APPROVAL

Name	Function
Hamard, Patrice	Division Marketing Manager
Nicholas, Jimmy Edward	Division Product Manager
Hannebert, Jean-Pierre	Division Q.A. Manager

Impacted Part Number			
QST104KT6	ST7FLIT10BF0B6	ST7FLITE25F2B6	ST7LNB0Y0M6
QST108KT6	ST7FLIT10BF0M6	ST7FLITE25F2M6	ST7LNB0Y0M6TR
ST72F260G1B5	ST7FLIT10BF1B6	ST7FLITE25M6TR	ST7LNB1Y0M6
ST72F260G1M6	ST7FLIT10BF1M6	ST7FLITE29F1B6	ST7LNB1Y0M6OOETR
ST72F260G1M6/TR	ST7FLIT10BM6TR	ST7FLITE29F2B6	ST7LNB1Y0M6ORITR
ST72F262G1B5	ST7FLIT10BY0B6	ST7FLITE29F2M6	ST7LNB1Y0M6/TR
ST72F262G1B6	ST7FLIT10BY0M6	ST7FLITE29F2M6TR	ST7OS15BF1M7/TR
ST72F262G1M6	ST7FLIT10BY1B6	ST7FLITE29F2M7TR	ST7PLITE05MSKTR
ST72F262G1M6TR	ST7FLIT10BY1M6	ST7FLITE49K2B6	ST7PLITE05NOBTR
ST72F262G2B5	ST7FLIT15BF0B6	ST7FLITE49K2T6	ST7PLITE05/OBNTR
ST72F262G2B6	ST7FLIT15BF0M3	ST7FLITE49K2T6TR	ST7PLITE05OBXTR
ST72F262G2M6	ST7FLIT15BF0M6	ST7FLITES2Y0B6	ST7PLITE05OJZTR
ST72F262G2M6TR	ST7FLIT15BF0M6TR	ST7FLITES2Y0M6	ST7PLITE05OMITR
ST72F264G1B5	ST7FLIT15BF1B6	ST7FLITES2YM6TR	ST7PLITE05OMJTR
ST72F264G1M6	ST7FLIT15BF1M3	ST7FLITES5Y0B6	ST7PLITE05OMKTR
ST72F264G2B5	ST7FLIT15BF1M6	ST7FLITES5Y0M6	ST7PLITE05ORSTR
ST72F264G2B6	ST7FLIT15BM6TR	ST7FLITES5Y0M6TR	ST7PLITE09MBRTR
ST72F264G2H1	ST7FLIT15BY0B6	ST7FLITEU02B6	ST7PLITE09OBMTR
ST72F264G2H1E	ST7FLIT15BY0M3	ST7FLITEU02M6	ST7PLITE29MYB/TR
ST72F264G2H6	ST7FLIT15BY0M6	ST7FLITEU05B6	ST7PLITES2/OKETR
ST72F264G2H6E	ST7FLIT15BY1B6	ST7FLITEU05M6	ST7PLITES2/ONBTR
ST72F264G2H6ETR	ST7FLIT15BY1M6	ST7FLITEU05M6TR	ST7PLITES5B6/OOT
ST72F264G2M6	ST7FLIT15Y1M6TR	ST7FLITEU09B3	ST7PLITES5/MLCTR
ST72F264G2M6/TR	ST7FLIT19BF0B6	ST7FLITEU09B6	ST7PLITES5OIHTR
ST72F340K2T6	ST7FLIT19BF0M3	ST7FLITEU09M3	ST7PLITEU0OSOTR
ST72F340K4T6	ST7FLIT19BF0M6	ST7FLITEU09M6	
ST72F340S2T6	ST7FLIT19BF1B6	ST7FLITEU09U3TR	
ST72F340S4T6	ST7FLIT19BF1M3	ST7FLITEU09U6TR	
ST72F344K2T6	ST7FLIT19BF1M6	ST7FLITEU0ICD	
ST72F344K4T6	ST7FLIT19BY0B6	ST7FLITEUSA2B3	
ST72F344K4T6TR	ST7FLIT19BY0M6	ST7FLITEUSA2B6	
ST72F344S2T6	ST7FLIT19BY1B6	ST7FLITEUSA2M3	
ST72F344S4T6	ST7FLIT19BY1M3	ST7FLITEUSA2M6	
ST72F344S4T6TR	ST7FLIT19BY1M6	ST7FLITEUSA2M6TR	
ST72F345C4T6	ST7FLIT19F1M6TR	ST7FLITEUSA2U6TR	
ST72F345C4T6TR	ST7FLIT19F1M7TR	ST7FLITEUSA5B6	
ST72P262M6/NAZTR	ST7FLITE02Y0B6	ST7FLITEUSA5M3	
ST72P262M6/NNOTR	ST7FLITE02Y0M6	ST7FLITEUSA5M3TR	
ST72P264H1/NYZTR	ST7FLITE05M6TR	ST7FLITEUSA5M6	
ST72P264H1/NYZTRE	ST7FLITE05Y0B6	ST7FLITEUSA5U6TR	
ST72P264M6/OOFTR	ST7FLITE05Y0M6	ST7FOXA0B6	
ST7FDALIF2M6	ST7FLITE09M6TR	ST7FOXA0M6	
ST7FDALIF2M6TR	ST7FLITE09Y0B6	ST7FOX1B6	
ST7FLI15BF1U6	ST7FLITE09Y0M6	ST7FOX1M6	
ST7FLI15BF1U6TR	ST7FLITE0HY0M6	ST7FOXK1B6	
ST7FLI19BF1M3TR	ST7FLITE10F1M6	ST7FOXK1T6	
ST7FLI19BF1U6TR	ST7FLITE15F1B6	ST7FOXK1T6TR	
ST7FLI19BY1M3TR	ST7FLITE15F1M6	ST7FOXK2B6	
ST7FLI49K2T6	ST7FLITE15M6TR	ST7FOXK2T6	
ST7FLI49MDIE6	ST7FLITE19F1B6	ST7FOXU0B6	
ST7FLI49MH1U6TR	ST7FLITE19F1M6	ST7FOXU0M6	
ST7FLI49MK1B6	ST7FLITE20F1B6	ST7LNB0V2Y0M6	
ST7FLI49MK1T3	ST7FLITE20F2B6	ST7LNB0V2Y0M6TR	
ST7FLI49MK1T6	ST7FLITE20F2M6	ST7LNB0V3Y0M6	
ST7FLI49MK1T6TR	ST7FLITE25F1B6	ST7LNB0V3Y0M6TR	



CMOSM6 EEPROM EMBEDDED TRANSFER TO CATANIA M5

RELIABILITY PLAN

MMS – Microcontroller Division
July 25th, 2008 V1.0

STMicroelectronics

CMOSM6X-F & CMOSM6D-F PRODUCT VEHICLES

**2 TEST VEHICLES WILL ALLOW TO QUALIFY
CMOSM6X-F & CMOSM6D-F PROCESS IN CATANIA M5**

	CMOSM6X-F	CMOSM6D-F
TEST VEHICLE	ST7FLITE0	ST7FLITE3
DIE NAME	819	852
PACKAGE ASSEMBLY SITE	SO16 MUAR	SO20 MUAR
QUALIFICATION LOTS	1 LOT	2 LOTS
RELIABILITY DURATION	14 weeks (1)	14 weeks (1)

(1) Starting at availability of finished good parts



CMOSM6X/D-F – RELIABILITY PLAN (1/2)

DIE ORIENTED RELIABILITY TRIALS

TEST	SPECIFICATIONS	NUMBER OF PARTS	ACCEPTANCE CRITERIA
ESD (Human Body Model)	AEC Q100 Method 002	1 LOT 6 PARTS	1500 Ω , 100 pF 2000 V / All leakage < 1 μ A
ESD (Charge Device Model)	AEC Q100 Method 003	1 LOT 6 PARTS	0 Ω , 200 pF 500 V / All leakage < 1 μ A
LATCH UP	AEC Q100 Method 004	1 LOT 6 PARTS	Overstress limits at 1.5 x VDDmax or 100 mA @ 125°C
EMC sensitivity	Internal spec 7053449 IEC1000-4-2 SAE J1752/3	Dynamic LU (+/- 1000V), EMI, Functional ESD	NA



CMOSM6X/D-F – RELIABILITY PLAN (2/2)

DIE ORIENTED RELIABILITY TRIALS

TEST	SPECIFICATIONS	NUMBER OF PARTS			ACCEPTANCE CRITERIA
		LOT 1	LOT 2	LOT 3	
EARLY FAILURE RATE	AEC Q100 Method 008	500	500	500	24 HRS / 140°C / 6V No reject with std Final Test
HIGH TEMPERATURE OPERATING LIFE TEST (1)	MIL Std 883E Method 1005	77	77	77	500 HRS / 140°C / 6V No reject with std Final Test
HIGH TEMPERATURE RETENTION BAKE (1)	MIL Std 883E Method 1008	77	77	77	1000 HRS / 175°C / No Bias No reject with std Final Test
PROGRAM EEPROM ENDURANCE CYCLING + POST RETENTION BAKE	ADCS 0061692	77	77	77	10K CYCLES @ 25°C + + BAKE 72 HRS @ 175°C No reject with std Final Test
DATA EEPROM ENDURANCE CYCLING (at Hot /Ambiant/ Cold temp) + POST RETENTION BAKE	ADCS 0061692	3*77	3*77	3*77	300K CYCLES at 25° 100K at 85°/ -40°C + + BAKE 72 HRS @ 175°C No reject with std Final Test

(1) Prior to this trial, parts are submitted to 10K cycles at 25°C.



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