

PRODUCT/PROCESS CHANGE NOTIFICATION

PCN APM-PMT/08/3262 Notification Date 01/23/2008

D2PAK PACKAGE: "COPPER-ON-COPPER" WIRE BONDING PROCESS CAPACITY EXTENSION

PMT - POWER MOSFET

Table 1. Change Implementation Schedule

Forecasted implementation date for change	15-Apr-2008
Forecasted availabillity date of samples for customer	16-Jan-2008
Forecasted date for STMicroelectronics change Qualification Plan results availability	16-Jan-2008
Estimated date of changed product first shipment	23-Apr-2008

Table 2. Change Identification

Product Identification (Product Family/Commercial Product)	See attached list
Type of change	Package assembly material change
Reason for change	Capacity Extension
Description of the change	Devices in D2PAK Package will be manufactured also in ST Assembly Plant of Shenzhen (China)
Product Line(s) and/or Part Number(s)	See attached
Description of the Qualification Plan	See attached
Change Product Identification	See letter "N" as additional info field
Manufacturing Location(s)	1]St Shenzhen -China

Table 3. List of Attachments

Customer Part numbers list	
Qualification Plan results	

Customer Acknowledgement of Receipt	PCN APM-PMT/08/3262
Please sign and return to STMicroelectronics Sales Office	Notification Date 01/23/2008
Qualification Plan Denied	Name:
Qualification Plan Approved	Title:
	Company:
🗖 Change Denied	Date:
Change Approved	Signature:
Remark	

Name	Function
Giudice, Maurizio	Division Marketing Manager
Wilson, Ian	Division Product Manager
Falcone, Giuseppe	Division Q.A. Manager

DOCUMENT APPROVAL

	APM CATANIA RELIABILITY REPORT	Date:	Oct '07
/ / ®	RELIABILITY REPORT	No	24/07

RELIABILITY EVALUATION ON

D²PAK

COPPER ON COPPER

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Introduction

This report aims at the qualification of the D²PAK package COPPER ON COPPER

The Qualification Reliability test trials have been performed in ST Catania Site.

The evaluation results meet ST products qualification targets, therefore the D^2PAK package COPPER ON COPPER is qualified.

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Test Vehicles :

Product Line Sales Type

Package

EZ62 STB4NK60ZT4

D²PAK

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Failure Criteria :

A failed component is a device which becomes inoperative during the test or it fails on meeting the end limits foreseen in the device specification, for one or more than the parameters here below reported

Parameter Power MOSFET

Drain Leakage Current (Idss) Gate Leakage Current (Igss) Threshold Voltage (Vgs(th)) Forward On Voltage (Vsd) Drain Source On Voltage (Vds(on)) Drain Source Breakdown Voltage (Bvdss)

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Reliability Evaluation Plan and results

D.U.T.: STB4NK60ZT4 LINE: EZ62 PACKAGE: D²PAK

Test	Conditions	S.S.	Requirement	Results
PRECONDITIONING OF SMD DEVICES BEFORE TC/THB/ENV. SEQ.	DRYNG 1H @ 125℃ STORE 168H @ TA=85℃ RH=85% Oven Reflow @ Tp=245℃ 3 times	204 x 1 Lot	Parameter deviation within spec. limits at end of precondition- ings.	No parameter deviation out of spec. limits at end of precondi- tionings.
H.T.S.	TA=150℃	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation out of spec. limits at 1000 hours.
Т.Н.В.	D.U.T. SMD PRECONDITIONED TA=85℃ - RH=85% Vbias= 100V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation out of spec. limits at 1000 hours.
H.T.R.B.	T.A.=150℃; Vdd=480V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation out of spec. limits at 1000 hours.
H.T.F.B.	TA = 150℃ ; Vgss= 30V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation out of spec. limits at 1000 hours.
PRESSURE POT	TA=121℃ - PA=2Atm	77 x 1 Lot	Parameter deviation within spec. limits at 96 hours.	No parameter deviation out of spec. limits at 96 hours.
THERMAL CYCLES AIR TO AIR	D.U.T. SMD PRECONDITIONED TA=-65℃ TO 150℃ 1 HOUR / CYCLE	77 x 1 Lot	Parameter deviation within spec. limits at 500 cycles.	No parameter deviation out of spec. limits at 500 cy
THERMAL FATIGUE	TC=105℃ - Pd=3.8W	77 x 1 Lot	Parameter deviation within spec. limits at 10k cycles.	No parameter deviation out of spec. limits at 10Kcy.
ENVIRONMENTAL SEQUENCE	D.U.T. SMD PRECONDITIONED 100 THERMAL CYCLES + 96H PP	50 x 1 Lot	Parameter deviation within spec. limits at end of test.	No parameter deviation out of spec. limits at end of test.



Technological Characteristics

D.U.T.: STB4NK60ZT4 LINE: EZ62 PACKAGE: D²PAK

DIE	Technology: Material: Metallization – Front :	Zener-Protected Su Silicon Al/Si (1%)	ıperMESH™ MOSFI Passivation : Dimensions :	NITRIDE
	- Back :	Ti/Ni/Au	Frame and lead material:	Copper
DIE ATTACH	Soft Solder	FRAME	Frame coating :	Selective Ni-Ni/P on Main Runner
			Lead coating :	Sn 100%
WIRE			Material :	Copper Gate Copper Source
BOND	Thermosonic	WIRE	Diameter :	2 mils Gate 2 mils Source
SEALING	Molding	PACKAGING	Material :	Epoxy Resin

PRODUCTION PLACES: WAFER PROCESSING: Ang Mokio (Singapore) ASSEMBLY LOCATION: SHENZHEN (China) QA LOCATION: SHENZHEN (China)

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Reliability Test Description

High Temperature Reverse Bias (HTRB)

This test is performed in order to demonstrate the quality and reliability of devices subjected to an elevated temperature and simultaneously reverse biased. The purpose of this test is to detect surface defects such as poor passivation, presence of contaminants, etc...

High Temperature Forward Bias (HTFB)

This test is performed in order to demonstrate the quality and reliability of devices subjected to an elevated temperature and simultaneously forward gate biased. The purpose of this test is to detect surface and gate oxide defects.

High Temperature Storage (HTS)

This stress test is performed to check the device life in a high temperature ambient. Specimens are put for a period of time inside a stove in free air. Detectable failure mechanisms are presence of contaminants and metal corrosion.

Temperature Humidity Bias (THB)

This test is performed to check the device life in a high humidity ambient. Specimens are subjected to a permanent bias in a climatic chamber in the presence of steam. Detectable failure mechanisms are metal corrosion and moulding defects.

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Reliability Test Description (continued)

Pressure Pot

This test is performed in order to check device life in a high humidity ambient in an accelerated way. Specimens are subjected for a period of time inside an autoclave in the presence of steam and pressure. Detectable failure mechanism is metal corrosion.

Thermal Fatigue

This test is performed to demonstrate the quality and reliability of devices exposed to cyclic variation in electrical stress between "on" and "off" conditions and resultant cyclic variation in device and case temperatures (thermo-mechanical stress). The purpose of this test is to detect assembly defects : improper die-attach, bonding weakness and thermal mismatch among various components of the package.

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