



PRODUCT/PROCESS CHANGE NOTIFICATION

PCN APM/08/3237
Notification Date 01/15/2008

**New Additional Assy-Testing in Subcontractor Fujitsu
(China) for Package SOT-223**

APM - APM

Table 1. Change Implementation Schedule

Forecasted implementation date for change	21-Apr-2008
Forecasted availability date of samples for customer	08-Jan-2008
Forecasted date for STMicroelectronics change Qualification Plan results availability	08-Jan-2008
Estimated date of changed product first shipment	28-Apr-2008

Table 2. Change Identification

Product Identification (Product Family/Commercial Product)	Power MOSFET and Power Bipolar
Type of change	Multiple types of changes
Reason for change	To improve service and increase our capacity
Description of the change	Following Divisionals commitment towards a continuous improvement of our service and in order to increase our capacity, it has been decided to set up in Subcontractor Fujitsu (China) a new Assembly and Testing location for all the P/Ns in SOT-223 package. Devices manufactured in Fujitsu (China) assembly plant are compliant with the RoHS. RoHS is a Restriction of the use of certain Hazardous Substances (European directive 2002/95/EC) and maintains the same Electrical and Mechanical characteristics as per current datasheets. At the moment FUJITSU is qualified subcontractor for other packages. Samples are available for lines used for Subcontractor qualification.
Product Line(s) and/or Part Number(s)	See attached
Description of the Qualification Plan	See attached
Change Product Identification	"F" as assy location code on package
Manufacturing Location(s)	

Table 3. List of Attachments

Customer Part numbers list	
Qualification Plan results	



Customer Acknowledgement of Receipt	PCN APM/08/3237					
Please sign and return to STMicroelectronics Sales Office	Notification Date 01/15/2008					
<input type="checkbox"/> Qualification Plan Denied <input type="checkbox"/> Qualification Plan Approved <input type="checkbox"/> Change Denied <input type="checkbox"/> Change Approved	<table border="1" style="width: 100%;"> <tr><td style="padding: 2px;">Name:</td></tr> <tr><td style="padding: 2px;">Title:</td></tr> <tr><td style="padding: 2px;">Company:</td></tr> <tr><td style="padding: 2px;">Date:</td></tr> <tr><td style="padding: 2px;">Signature:</td></tr> </table>	Name:	Title:	Company:	Date:	Signature:
Name:						
Title:						
Company:						
Date:						
Signature:						
Remark						

DOCUMENT APPROVAL

Name	Function
Di falco, Luca	Division Marketing Manager
Giudice, Maurizio	Division Marketing Manager
Porto, Michele Claudio	Division Product Manager
Wilson, Ian	Division Product Manager
Falcone, Giuseppe	Division Q.A. Manager

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

RELIABILITY EVALUATION ON

SOT-223

MADE IN FUJITSU

ISSUED BY	RELIABILITY DEPARTMENT	Page 1 of 23
------------------	-----------------------------------	---------------------

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Table of Contents

1.	Introduction	pg. 3
2.	Test vehicles	pg. 4
3.	Failure Criteria	pg. 5
4.	Evaluation plan and results	pg. 6
5.	Appendixes	
	- Technological Characteristics	pg. 14
	- Reliability Test Description	pg. 22

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Introduction

This report aims at the internal qualification of the SOT-223 package made in Fujitsu.

The Qualification Reliability test trials have been performed in ST Catania Site.

The evaluation results meet ST products qualification targets, therefore the SOT-223 package made in Fujitsu is qualified.

ISSUED BY	RELIABILITY DEPARTMENT	Page 3 of 23
------------------	-----------------------------------	---------------------

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Test Vehicles:

Product Lines Power MOSFET	Sales Type	Package
EC6M	STN1HNK60	SOT-223
EZ6P	STN1NK60Z	SOT-223
EZ8Q	STN1NK80Z	SOT-223
E33N	STN4NF03L	SOT-223
AL6A	STN3NF06L	SOT-223
AL6A	STN4NF06L	SOT-223

Product Lines Power Bipolar	Sales Type	Package
F817	BCP53-16	SOT-223
B118	2STN2540	SOT-223

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Failure Criteria :

A failed component is a device which becomes inoperative during the test or it fails on meeting the end limits foreseen in the device specification, for one or more than the parameters here below reported

Parameters Power MOSFET

- Drain Leakage Current (I_{dss})
- Gate Leakage Current (I_{gss})
- Threshold Voltage ($V_{gs(th)}$)
- Forward On Voltage (V_{sd})
- Drain Source On Voltage ($V_{ds(on)}$)
- Drain Source Breakdown Voltage (V_{dss})

Parameters Power Bipolar

- Collector Leakage Current (I_{cbo} or I_{ceo} or I_{ces} , etc...)
- Emitter Leakage (I_{ebo})
- H_{FE} , V_{cesat} , V_{besat} , V_f
- Breakdown Voltage (V_{cbo} , V_{ceo} , V_{bces} , V_{bebo})

ISSUED BY	RELIABILITY DEPARTMENT	Page 5 of 23
------------------	-----------------------------------	---------------------

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Reliability Evaluation Plan and results

D.U.T.: STN1HNK60 LINE: EC6M PACKAGE: SOT-223

Test	Conditions	S.S.	Requirement	Results
PRECONDITIONING OF SMD DEVICES BEFORE TC/THB/ENV. SEQ.	DRYNG 1H @ 125°C STORE 168H @ TA=85°C RH=85% IR @ Tp=260°C 3 times	204 x 1 Lot	Parameter deviation within spec. limits at end of preconditionings.	No parameter deviation at end of preconditionings.
H.T.S.	TA=150°C	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
T.H.B.	<i>D.U.T. SMD PRECONDITIONED</i> TA=85°C - RH=85% Vbias= 100V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
H.T.R.B.	T.A.= 150°C Vdd = 480 V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
H.T.F.B.	Ta=150°C Vgss= 30V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
PRESSURE POT	TA=121°C - PA=2Atm	77 x 1 Lot	Parameter deviation within spec. limits at 96hours.	No parameter deviation at 96 hours.
THERMAL CYCLES AIR TO AIR	<i>D.U.T. SMD PRECONDITIONED</i> TA=-65°C TO 150°C 1 HOUR / CYCLE	77 x 1 Lot	Parameter deviation within spec. limits at 500 cycles.	No parameter deviation at 500 cy
ENVIRONMENTAL SEQUENCE	<i>D.U.T. SMD PRECONDITIONED</i> 100 THERMAL CYCLES + 168H PP	50 x 1 Lot	Parameter deviation within spec. limits at end of test.	No parameter deviation at end of test.

ISSUED BY	RELIABILITY DEPARTMENT	Page 6 of 23
------------------	-------------------------------	---------------------

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Reliability Evaluation Plan and results

D.U.T.: STN1NK60Z LINE: EZ6P PACKAGE: SOT-223

Test	Conditions	S.S.	Requirement	Results
PRECONDITIONING OF SMD DEVICES BEFORE TC/THB/ENV. SEQ.	DRYNG 1H @ 125°C STORE 168H @ TA=85°C RH=85% IR @ Tp=260°C 3 times	204 x 1 Lot	Parameter deviation within spec. limits at end of preconditionings.	No parameter deviation at end of preconditionings.
H.T.S.	TA=150°C	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
T.H.B.	<i>D.U.T. SMD PRECONDITIONED</i> TA=85°C - RH=85% Vbias= 100V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
H.T.R.B.	T.A.= 150°C Vdd = 480 V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
H.T.F.B.	Ta=150°C Vgss= 30V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
PRESSURE POT	TA=121°C - PA=2Atm	77 x 1 Lot	Parameter deviation within spec. limits at 96hours.	No parameter deviation at 96 hours.
THERMAL CYCLES AIR TO AIR	<i>D.U.T. SMD PRECONDITIONED</i> TA=-65°C TO 150°C 1 HOUR / CYCLE	77 x 1 Lot	Parameter deviation within spec. limits at 500 cycles.	No parameter deviation at 500 cy
ENVIRONMENTAL SEQUENCE	<i>D.U.T. SMD PRECONDITIONED</i> 100 THERMAL CYCLES + 168H PP	50 x 1 Lot	Parameter deviation within spec. limits at end of test.	No parameter deviation at end of test.

ISSUED BY	RELIABILITY DEPARTMENT	Page 7 of 23
------------------	-------------------------------	---------------------

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Reliability Evaluation Plan and results

D.U.T.: STN1NK80Z LINE: EZ8Q PACKAGE: SOT-223

Test	Conditions	S.S.	Requirement	Results
PRECONDITIONING OF SMD DEVICES BEFORE TC/THB/ENV. SEQ.	DRYNG 1H @ 125°C STORE 168H @ TA=85°C RH=85% IR @ Tp=260°C 3 times	204 x 1 Lot	Parameter deviation within spec. limits at end of preconditionings.	No parameter deviation at end of preconditionings.
H.T.S.	TA=150°C	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
T.H.B.	<i>D.U.T. SMD PRECONDITIONED</i> TA=85°C - RH=85% Vbias= 100V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
H.T.R.B.	T.A.= 150°C Vdd = 640 V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
H.T.F.B.	Ta=150°C Vgss= 30V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
PRESSURE POT	TA=121°C - PA=2Atm	77 x 1 Lot	Parameter deviation within spec. limits at 96 hours.	No parameter deviation at 96 hours.
THERMAL CYCLES AIR TO AIR	<i>D.U.T. SMD PRECONDITIONED</i> TA=-65°C TO 150°C 1 HOUR / CYCLE	77 x 1 Lot	Parameter deviation within spec. limits at 500 cycles.	No parameter deviation at 500 cy
ENVIRONMENTAL SEQUENCE	<i>D.U.T. SMD PRECONDITIONED</i> 100 THERMAL CYCLES + 168H PP	50 x 1 Lot	Parameter deviation within spec. limits at end of test.	No parameter deviation at end of test.

ISSUED BY	RELIABILITY DEPARTMENT	Page 8 of 23
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	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Reliability Evaluation Plan and results

D.U.T.: STN4NF03L LINE: E33N PACKAGE: SOT-223

Test	Conditions	S.S.	Requirement	Results
PRECONDITIONING OF SMD DEVICES BEFORE TC/THB/ENV. SEQ.	DRYNG 1H @ 125°C STORE 168H @ TA=85°C RH=85% IR @ Tp=260°C 3 times	204 x 1 Lot	Parameter deviation within spec. limits at end of preconditionings.	No parameter deviation at end of preconditionings.
H.T.S.	TA=150°C	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
T.H.B.	D.U.T. SMD PRECONDITIONED TA=85°C - RH=85% Vbias= 24V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
H.T.R.B.	T.A.= 150°C Vdd = 24V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
H.T.F.B.	TA=150°C Vgss=16V	77x1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
PRESSURE POT	TA=121°C – PA=2Atm	77 x 1 Lot	Parameter deviation within spec. limits at 96 hours.	No parameter deviation at 96 hours.
THERMAL CYCLES AIR TO AIR	D.U.T. SMD PRECONDITIONED TA=-65°C TO 150°C 1 HOUR / CYCLE	77 x 1 Lot	Parameter deviation within spec. limits at 500 cycles.	No parameter deviation at 500 cy
ENVIRONMENTAL SEQUENCE	D.U.T. SMD PRECONDITIONED 100 THERMAL CYCLES + 168H PP	50 x 1 Lot	Parameter deviation within spec. limits at end of test.	No parameter deviation at end of test.

ISSUED BY	RELIABILITY DEPARTMENT	Page 9 of 23
------------------	-------------------------------	---------------------

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Reliability Evaluation Plan and results

D.U.T.: STN3NF06L LINE: AL6A PACKAGE: SOT-223

Test	Conditions	S.S.	Requirement	Results
PRECONDITIONING OF SMD DEVICES BEFORE TC/THB/ENV. SEQ.	DRYNG 1H @ 125°C STORE 168H @ TA=85°C RH=85% IR @ Tp=260°C 3 times C	204 x 1 Lot	Parameter deviation within spec. limits at end of preconditionings.	No parameter deviation at end of preconditionings.
H.T.S.	TA=150°C	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
T.H.B.	<i>D.U.T. SMD PRECONDITIONED</i> TA=85°C - RH=85% Vbias= 50V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours
H.T.R.B.	T.A.= 150°C Vdd = 48V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours
H.T.F.B.	TA=150°C Vgss=16V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours
PRESSURE POT	TA=121°C - PA=2Atm	77 x 1 Lot	Parameter deviation within spec. limits at 96 hours.	No parameter deviation at 96 hours. .
THERMAL CYCLES AIR TO AIR	<i>D.U.T. SMD PRECONDITIONED</i> TA=-65°C TO 150°C 1 HOUR / CYCLE	77 x 1 Lot	Parameter deviation within spec. limits at 500 cycles.	No parameter deviation at 500 cy
THERMAL FATIGUE	ΔTC=105°C - Pd=2W	77 x 1 Lot	Parameter deviation within spec. limits at 10k cycles.	No parameter deviation at 10Kcy.
ENVIRONMENTAL SEQUENCE	<i>D.U.T. SMD PRECONDITIONED</i> 100 THERMAL CYCLES + 168H PP	50 x 1 Lot	Parameter deviation within spec. limits at end of test.	No parameter deviation at end of test.

ISSUED BY	RELIABILITY DEPARTMENT	Page 10 of 23
------------------	-------------------------------	----------------------

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Reliability Evaluation Plan and results

D.U.T.: STN4NF06L LINE:AL6A PACKAGE: SOT-223

Test	Conditions	S.S.	Requirement	Results
PRECONDITIONING OF SMD DEVICES BEFORE TC/THB/ENV. SEQ.	DRYNG 1H @ 125°C STORE 168H @ TA=85°C RH=85% IR @ Tp=260°C 3 times	204 x 1 Lot	Parameter deviation within spec. limits at end of preconditionings.	No parameter deviation at end of preconditionings.
H.T.S.	TA=150°C	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
T.H.B.	<i>D.U.T. SMD PRECONDITIONED</i> TA=85°C - RH=85% Vbias= 50V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours
H.T.R.B.	T.A.= 150°C Vdd = 48V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours
H.T.F.B.	TA=150°C Vgss=16V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours
PRESSURE POT	TA=121°C - PA=2Atm	77 x 1 Lot	Parameter deviation within spec. limits at 96 hours.	No parameter deviation at 96 hours. .
THERMAL CYCLES AIR TO AIR	<i>D.U.T. SMD PRECONDITIONED</i> TA=-65°C TO 150°C 1 HOUR / CYCLE	77 x 1 Lot	Parameter deviation within spec. limits at 500 cycles.	No parameter deviation at 500 cy
THERMAL FATIGUE	ΔTC=105°C - Pd=2W	77 x 1 Lot	Parameter deviation within spec. limits at 10k cycles.	No parameter deviation at 10Kcy.
ENVIRONMENTAL SEQUENCE	<i>D.U.T. SMD PRECONDITIONED</i> 100 THERMAL CYCLES + 168H PP	50 x 1 Lot	Parameter deviation within spec. limits at end of test.	No parameter deviation at end of test.

ISSUED BY	RELIABILITY DEPARTMENT	Page 11 of 23
------------------	-------------------------------	----------------------

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Reliability Evaluation Plan and results

D.U.T.: BCP53-16 LINE: F817 PACKAGE: SOT-223

Test	Conditions	S.S.	Requirement	Results
PRECONDITIONING OF SMD DEVICES BEFORE TC/THB/ENV. SEQ.	DRYNG 1H @ 125°C STORE 168H @ TA=85°C RH=85% IR @ Tp=260°C 3 times	204 x 1 Lot	Parameter deviation within spec. limits at end of preconditionings.	No parameter deviation out of spec. Limits at end of preconditionings.
H.T.S.	TA=150°C	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation out of spec. limits at 1000 hours.
T.H.B.	<i>D.U.T. SMD PRECONDITIONED</i> TA=85°C - RH=85% Vbias= 50V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation out of spec. limits at 1000 hours.
H.T.R.B.	T.A.= 150°C Vces = -80V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation out of spec. limits at 1000 hours.
PRESSURE POT	TA=121°C - PA=2Atm	77 x 1 Lot	Parameter deviation within spec. limits at 168 hours.	No parameter deviation out of spec. limits at 168 hours.
THERMAL CYCLES AIR TO AIR	<i>D.U.T. SMD PRECONDITIONED</i> TA=-65°C TO 150°C 1 HOUR / CYCLE	77 x 1 Lot	Parameter deviation within spec. limits at 500 cycles.	No parameter deviation out of spec. limits at 500 cy.
ENVIRONMENTAL SEQUENCE	<i>D.U.T. SMD PRECONDITIONED</i> 100 THERMAL CYCLES + 168H PP	50 x 1 Lot	Parameter deviation within spec. limits at end of test.	No parameter deviation out of spec. limits at end of test.

ISSUED BY	RELIABILITY DEPARTMENT	Page 12 of 23
------------------	-------------------------------	----------------------

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Reliability Evaluation Plan and results

D.U.T.: 2STN2540 LINE: BI18 PACKAGE: SOT-223

Test	Conditions	S.S.	Requirement	Results
PRECONDITIONING OF SMD DEVICES BEFORE TC/THB/ENV. SEQ.	DRYNG 1H @ 125°C STORE 168H @ TA=85°C RH=85% IR @ Tp=260°C 3 times	204 x 1 Lot	Parameter deviation within spec. limits at end of preconditionings.	No parameter deviation at end of preconditionings.
H.T.S.	TA=150°C	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
T.H.B.	D.U.T. SMD PRECONDITIONED TA=85°C - RH=85% Vbias= -30V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
H.T.R.B.	T.A.= 150°C Vces = -32V	77 x 1 Lot	Parameter deviation within spec. limits at 1000 hours.	No parameter deviation at 1000 hours.
PRESSURE POT	TA=121°C - PA=2Atm	77 x 1 Lot	Parameter deviation within spec. limits at 96 hours.	No parameter deviation at 96 hours.
THERMAL CYCLES AIR TO AIR	D.U.T. SMD PRECONDITIONED TA=-65°C TO 150°C 1 HOUR / CYCLE	77 x 1 Lot	Parameter deviation within spec. limits at 500 cycles.	No parameter deviation at 500 cy
ENVIRONMENTAL SEQUENCE	D.U.T. SMD PRECONDITIONED 100 THERMAL CYCLES + 168H PP	50 x 1 Lot	Parameter deviation within spec. limits at end of test.	No parameter deviation at end of test.

ISSUED BY	RELIABILITY DEPARTMENT	Page 13 of 23
------------------	-------------------------------	----------------------

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Technological Characteristics

D.U.T.: STN1HNK60 LINE: EC6M PACKAGE: SOT-223

DIE	<i>Technology:</i> SuperMESH™ MOSFET <i>Material:</i> Silicon <i>Passivation :</i> Nitride <i>Metallization – Front :</i> Al/Si (1%) <i>Dimensions :</i> 2030 x 1730 μm ² <i>- Back :</i> Ti-Ni-Au			
DIE ATTACH	Soft Solder Pb/Sn/Ag	FRAME	<i>Frame and lead material:</i> <i>Frame coating :</i> <i>Lead coating :</i>	Raw Copper Ag Spot on die pad and leads No coating Sn 100%
WIRE BOND	Thermosonic	WIRE	<i>Material :</i> <i>Diameter :</i>	Au Gate Au Source 2 mils Gate 2 mils Source
SEALING	Molding	PACKAGING	<i>Material :</i>	Epoxy Resin

PRODUCTION PLACES : WAFER PROCESSING : Ang Mokia (Singapore)
 ASSEMBLY LOCATION : Fujitsu (China)
 Q.A. LOCATION : Fujitsu (China)

ISSUED BY	RELIABILITY DEPARTMENT	Page 14 of 23
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	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Technological Characteristics

D.U.T.: STN1NK60Z Line: EZ6P Package: SOT223

DIE	<i>Technology:</i> SuperMESH™ Power MOSFET <i>Material:</i> Silicon <i>Passivation :</i> Nitride <i>Metallization – Front :</i> Al/Si (1%) <i>Dimensions :</i> 1890 x 1140 μm ² <i>- Back :</i> Ti-Ni-Au			
DIE ATTACH	Soft Solder Pb/Sn/Ag	FRAME	<i>Frame and lead material:</i> <i>Frame coating :</i> <i>Lead coating :</i>	Raw Copper Ag Spot on die pad and leads No coating Sn 100%
WIRE BOND	Thermosonic	WIRE	<i>Material :</i> <i>Diameter :</i>	Au Gate Au Source 2 mils Gate 2 mils Source
SEALING	Molding	PACKAGING	<i>Material :</i>	Epoxy Resin

PRODUCTION PLACES : WAFER PROCESSING : Ang Mokie (Singapore)
ASSEMBLY LOCATION : Fujitsu (China)
Q.A. LOCATION : Fujitsu (China)

ISSUED BY	RELIABILITY DEPARTMENT	Page 15 of 23
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	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Technological Characteristics

D.U.T.: STN1NK80Z LINE: EZ8Q PACKAGE: SOT-223

DIE	<i>Technology:</i> SuperMESH™ MOSFET <i>Material:</i> Silicon <i>Passivation :</i> Nitride <i>Metallization – Front :</i> Al/Si (1%) <i>Dimensions :</i> 2230 x 1760 μm ² <i>- Back :</i> Ti-Ni-Au			
DIE ATTACH	Soft Solder Pb/Sn/Ag	FRAME	<i>Frame and lead material:</i> <i>Frame coating :</i> <i>Lead coating :</i>	Raw Copper Ag Spot on die pad and leads No coating Sn 100%
WIRE BOND	Thermosonic	WIRE	<i>Material :</i> <i>Diameter :</i>	Au Gate Au Source 2 mils Gate 2 mils Source
SEALING	Molding	PACKAGING	<i>Material :</i>	Epoxy Resin

PRODUCTION PLACES : WAFER PROCESSING : Ang Mokie (Singapore)
ASSEMBLY LOCATION : Fujitsu (China)
Q.A. LOCATION : Fujitsu (China)

ISSUED BY	RELIABILITY DEPARTMENT	Page 16 of 23
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	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Technological Characteristics

D.U.T.: STN4NF03L Line: E33N Package: SOT223

DIE	<i>Technology:</i> STripFET II POWER MOSFET <i>Material:</i> Silicon <i>Passivation :</i> No passivation <i>Metallization – Front :</i> Al/Si (1%) <i>Dimensions :</i> 1750 x 1270 µm ² <i>- Back :</i> Ti-Ni-Au			
DIE ATTACH	Soft Solder Pb/Sn/Ag	FRAME	<i>Frame and lead material:</i>	Raw Copper Ag Spot on die pad and leads
			<i>Frame coating :</i>	No coating
			<i>Lead coating :</i>	Sn 100%
WIRE BOND	Thermosonic	WIRE	<i>Material :</i>	Au Gate Au Source
			<i>Diameter :</i>	2 mils Gate 2 mils Source
SEALING	Molding	PACKAGING	<i>Material :</i>	Epoxy Resin

PRODUCTION PLACES : WAFER PROCESSING : Ang Mokia (Singapore)
ASSEMBLY LOCATION : Fujitsu (China)
Q.A. LOCATION : Fujitsu (China)

ISSUED BY	RELIABILITY DEPARTMENT	Page 17 of 23
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	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Technological Characteristics

D.U.T.: STN3NF06L **Line:** AL6A **Package:** SOT-223

DIE	<i>Technology:</i> N- Channel Enhancement Mode Power MOSFET <i>Material:</i> Silicon <i>Passivation :</i> No passivation <i>Metallization – Front :</i> Al/Si (1%) <i>Dimensions :</i> 1700 x 1360 μm ² <i>- Back :</i> Ti-Ni-Au			
DIE ATTACH	Soft Solder Pb/Sn/Ag	FRAME	<i>Frame and lead material:</i> <i>Frame coating :</i> <i>Lead coating :</i>	Raw Copper Ag Spot on die pad and leads No coating Sn 100%
WIRE BOND	Thermosonic	WIRE	<i>Material :</i> <i>Diameter :</i>	Au Gate Au Source 2 mils Gate 2 mils Source
SEALING	Molding	PACKAGING	<i>Material :</i>	Epoxy Resin

PRODUCTION PLACES : WAFER PROCESSING : Ang Mokio (Singapore)
ASSEMBLY LOCATION : Fujitsu (China)
Q.A. LOCATION : Fujitsu (China)

ISSUED BY	RELIABILITY DEPARTMENT	Page 18 of 23
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	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Technological Characteristics

D.U.T.: STN4NF06L Line: AL6A Package: SOT-223

DIE	<i>Technology:</i> N- Channel Enhancement Mode Power MOSFET <i>Material:</i> Silicon <i>Passivation :</i> No passivation <i>Metallization – Front :</i> Al/Si (1%) <i>Dimensions :</i> 1700 x 1360 μm ² - Back : Ti-Ni-Au			
DIE ATTACH	Soft Solder Pb/Sn/Ag	FRAME	<i>Frame and lead material:</i> <i>Frame coating :</i> <i>Lead coating :</i>	Raw Copper Ag Spot on die pad and leads No coating Sn 100%
WIRE BOND	Thermosonic	WIRE	<i>Material :</i> <i>Diameter :</i>	Au Gate Au Source 2 mils Gate 2 mils Source
SEALING	Molding	PACKAGING	<i>Material :</i>	Epoxy Resin

PRODUCTION PLACES : WAFER PROCESSING : Ang Mokia (Singapore)
ASSEMBLY LOCATION : Fujitsu (China)
Q.A. LOCATION : Fujitsu (China)

ISSUED BY	RELIABILITY DEPARTMENT	Page 19 of 23
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**APM CATANIA
RELIABILITY REPORT**

Date: Dec '07
No 27/07

Technological Characteristics

D.U.T.: BCP53-16 Line: F817 Package: SOT-223

DIE	<i>Technology:</i> Planar PNP Transistor <i>Material:</i> Silicon <i>Passivation :</i> P-Vapox <i>Metallization – Front :</i> Al/Si <i>Dimensions :</i> 1070 x 1070 μm^2 <i>- Back :</i> Au-Cr-Ni-Au			
DIE ATTACH	Soft Solder Pb/Sn/Ag	FRAME	<i>Frame and lead material:</i>	Raw Copper Ag Spot on die pad and leads
			<i>Frame coating :</i>	No coating
			<i>Lead coating :</i>	Sn 100%
WIRE BOND	Thermosonic	WIRE	<i>Material :</i>	Au Base Au Emitter
			<i>Diameter :</i>	2 mils Base 2 mils Emitter
SEALING	Molding	PACKAGING	<i>Material :</i>	Epoxy Resin

PRODUCTION PLACES : WAFER PROCESSING : Ang Mokia (Singapore)
ASSEMBLY LOCATION : Fujitsu (China)
Q.A. LOCATION : Fujitsu (China)

	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Technological Characteristics

D.U.T.: 2STN2540 Line: BI18 Package: SOT-223

DIE	<i>Technology:</i> Planar PNP Transistor <i>Material:</i> Silicon <i>Passivation :</i> P-Vapox <i>Metallization – Front :</i> Al/Si <i>Dimensions :</i> 1780 x 1770 µm ² <i>- Back :</i> Au-Cr-Ni-Au			
DIE ATTACH	Soft Solder Pb/Sn/Ag	FRAME	<i>Frame and lead material:</i> <i>Frame coating :</i> <i>Lead coating :</i>	Raw Copper Ag Spot on die pad and leads No coating Sn 100%
WIRE BOND	Thermosonic	WIRE	<i>Material :</i> <i>Diameter :</i>	Au Base Au Emitter 2 mils Base 2 mils Emitter
SEALING	Molding	PACKAGING	<i>Material :</i>	Epoxy Resin

PRODUCTION PLACES : WAFER PROCESSING : Ang Mokie (Singapore)
ASSEMBLY LOCATION : Fujitsu (China)
Q.A. LOCATION : Fujitsu (China)

ISSUED BY	RELIABILITY DEPARTMENT	Page 21 of 23
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	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Reliability Test Description

High Temperature Reverse Bias (HTRB)

This test is performed in order to demonstrate the quality and reliability of devices subjected to an elevated temperature and simultaneously reverse biased. The purpose of this test is to detect surface defects such as poor passivation, presence of contaminants, etc...

High Temperature Forward Bias (HTFB)

This test is performed in order to demonstrate the quality and reliability of devices subjected to an elevated temperature and simultaneously forward gate biased. The purpose of this test is to detect surface and gate oxide defects.

High Temperature Storage (HTS)

This stress test is performed to check the device life in a high temperature ambient. Specimens are put for a period of time inside a stove in free air. Detectable failure mechanisms are presence of contaminants and metal corrosion.

Thermal Cycles/Shocks

The purpose of this test is to determine the resistance of devices to exposure to extreme changes in temperature. Specimens are first placed in a suitable environment at a low temperature and then transferred to one at high temperature. Effects of thermal cycles/shocks include cracking of die, breaking of wire bonding, mechanical damage to the device case.

ISSUED BY	RELIABILITY DEPARTMENT	Page 22 of 23
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	APM CATANIA RELIABILITY REPORT	Date:	Dec '07
		No	27/07

Reliability Test Description (continued)

Temperature Humidity Bias (THB)

This test is performed to check the device life in a high humidity ambient. Specimens are subjected to a permanent bias in a climatic chamber in the presence of steam. Detectable failure mechanisms are metal corrosion and moulding defects.

Pressure Pot

This test is performed in order to check device life in a high humidity ambient in an accelerated way. Specimens are subjected for a period of time inside an autoclave in the presence of steam and pressure. Detectable failure mechanism is metal corrosion.

Thermal Fatigue

This test is performed to demonstrate the quality and reliability of devices exposed to cyclic variation in electrical stress between "on" and "off" conditions and resultant cyclic variation in device and case temperatures (thermo-mechanical stress). The purpose of this test is to detect assembly defects : improper die-attach, bonding weakness and thermal mismatch among various components of the package.

Environmental Sequence

The purpose of this test is to study the influence of corrosion mechanism when the die/package system has already been stressed by temperature cycling.

ISSUED BY	RELIABILITY DEPARTMENT	Page 23 of 23
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